

100

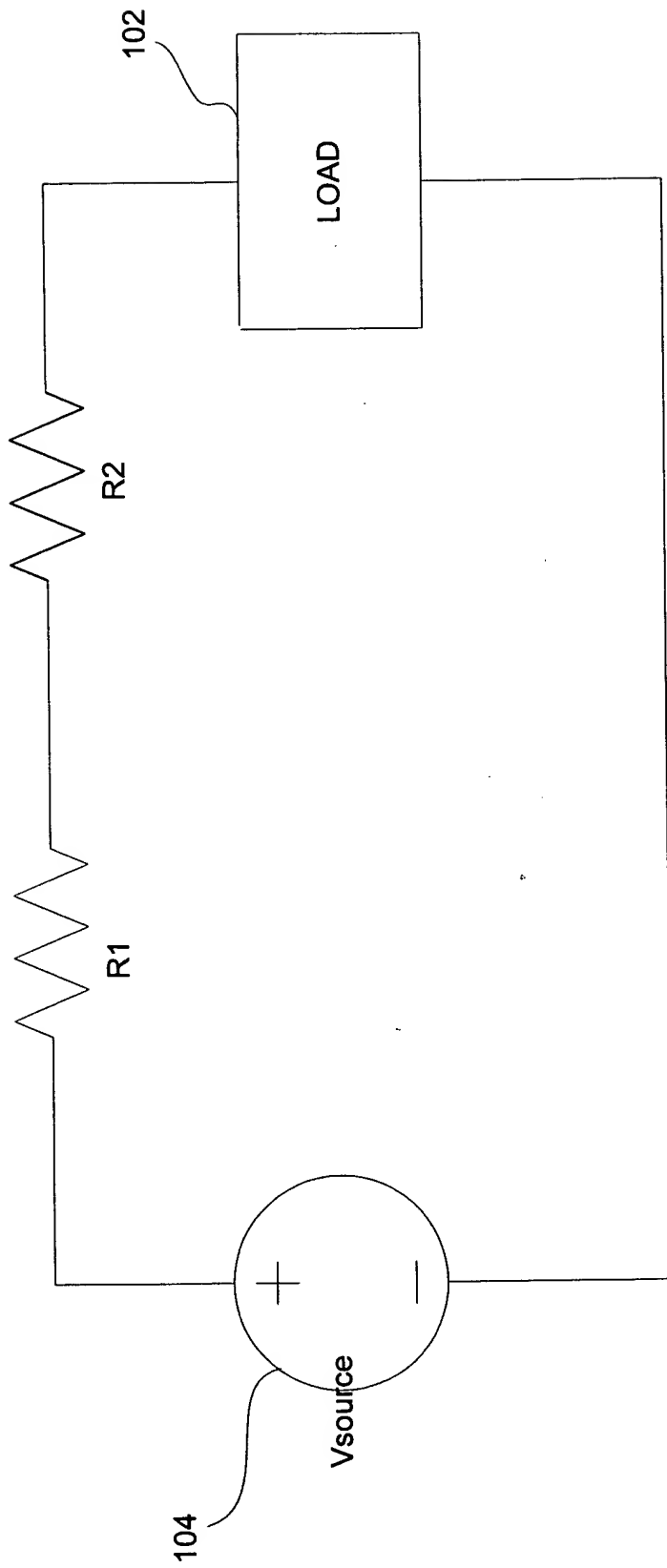


FIG. 1 (PRIOR ART)

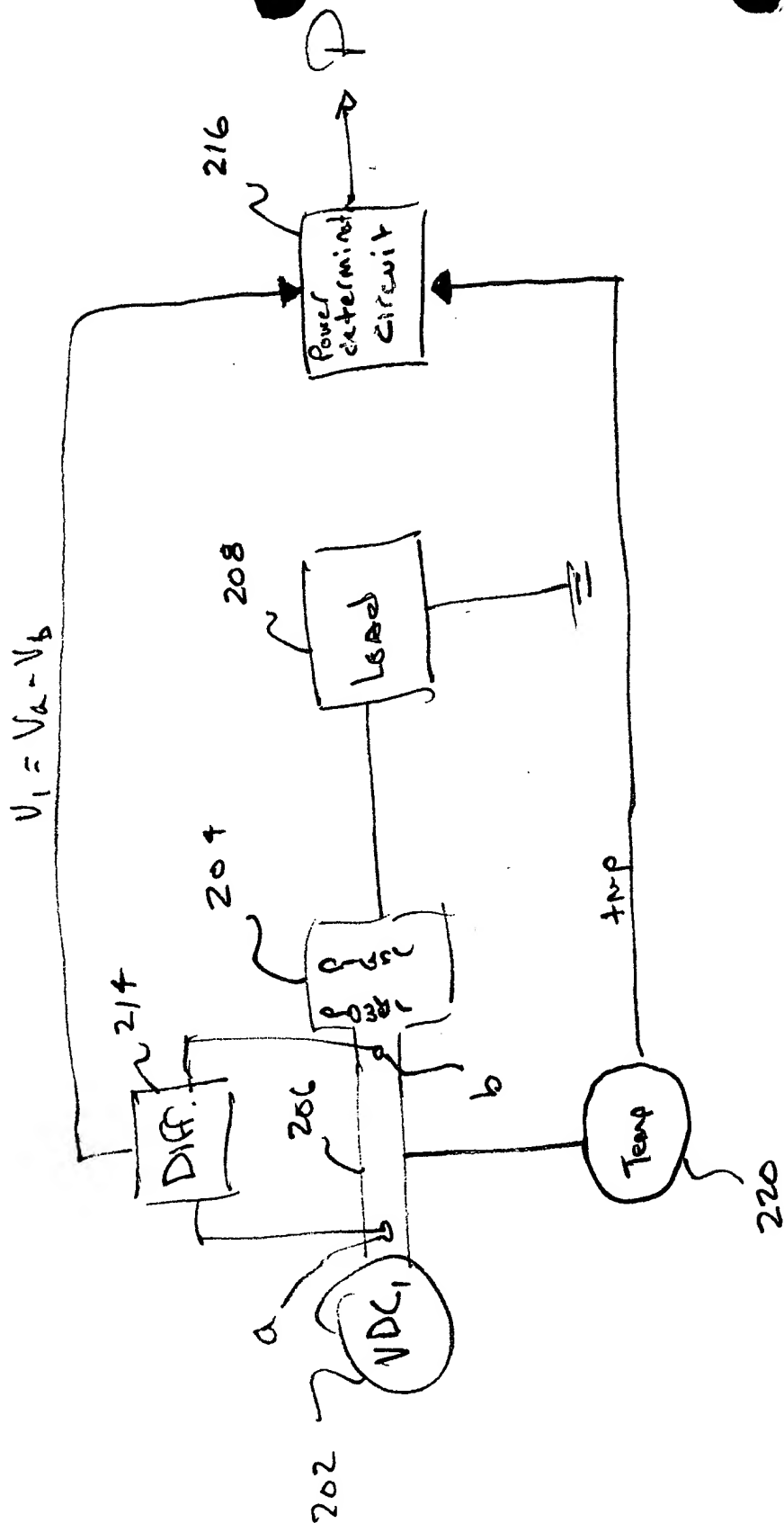


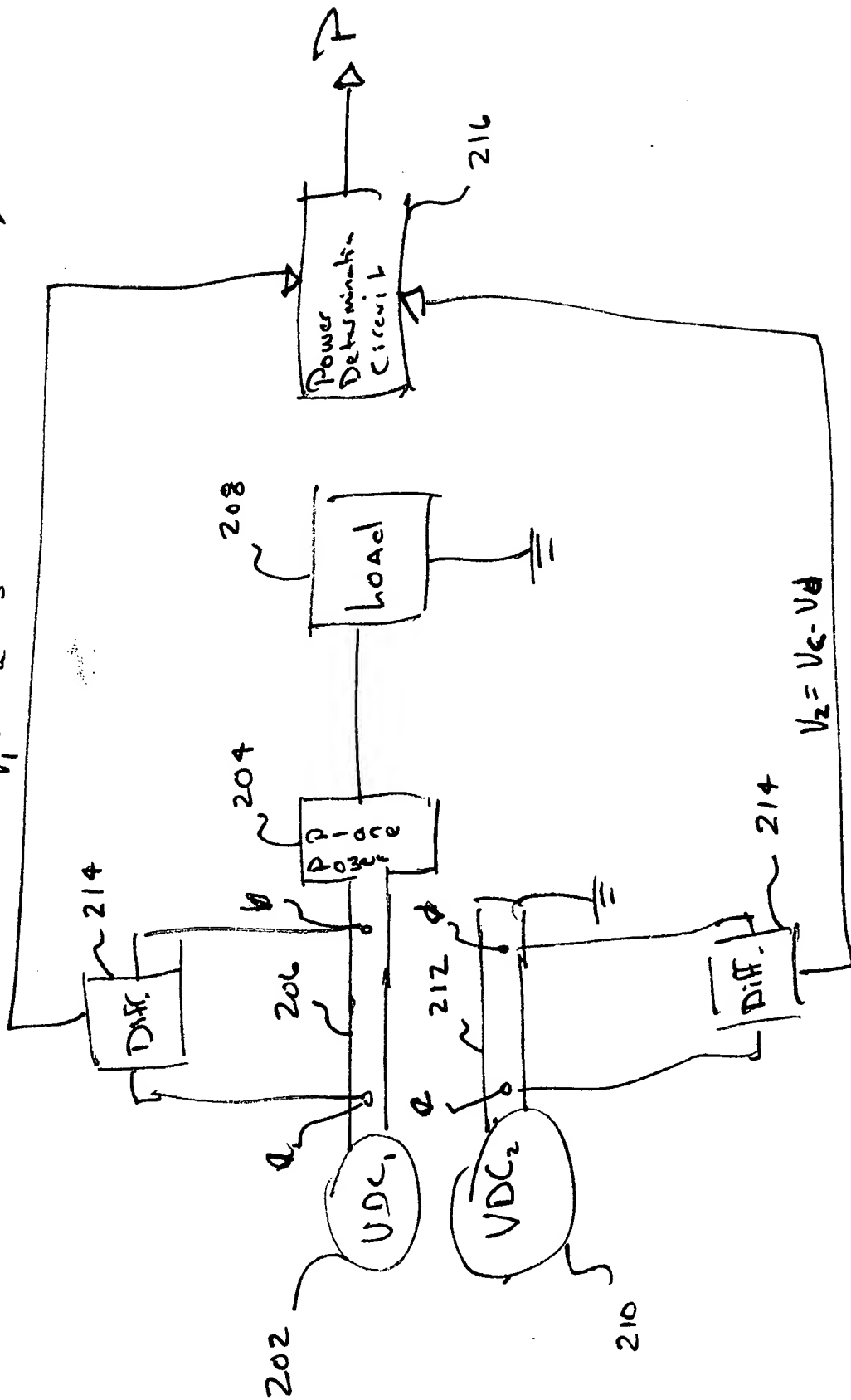
FIG. 2

The diagram illustrates a power plane 204 with a strip 206. A circular via 202 is shown on the left, labeled "Vias from Power Supply Source on top layer to Power Plane." The strip 206 is a rectangular region with two circular vias, V0 and V1, spaced by a length L. The voltage drop across the strip is given by  $V_{drop} = V1 - V0$ . A 3D view of the strip shows its length L, width w, and thickness t. The strip is connected to the power plane 204 via vias.

F16. 3

200

$$V_1 = V_c - V_b$$



$$V_2 = V_c - V_d$$

FIG. 4

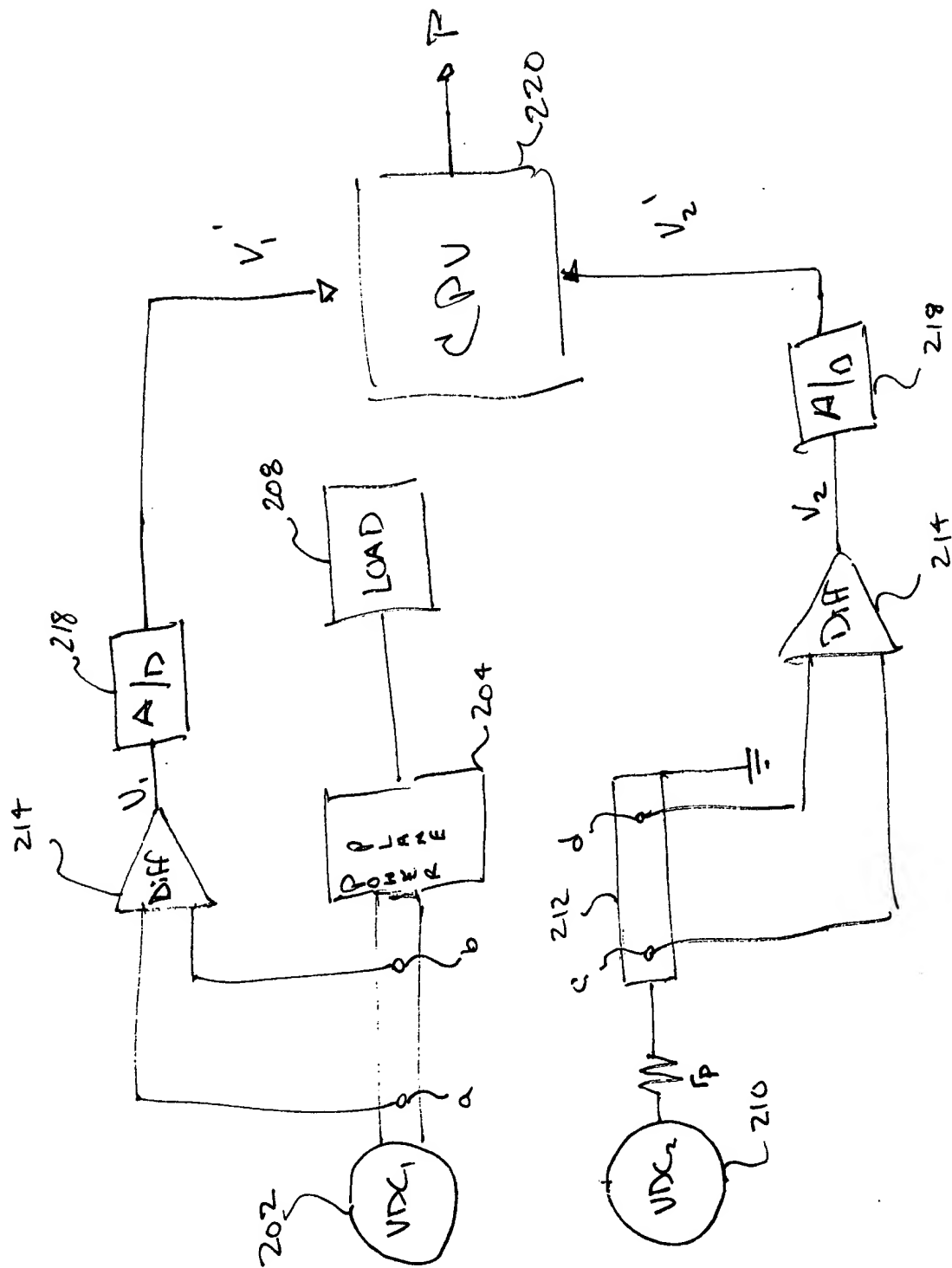


FIG 5a

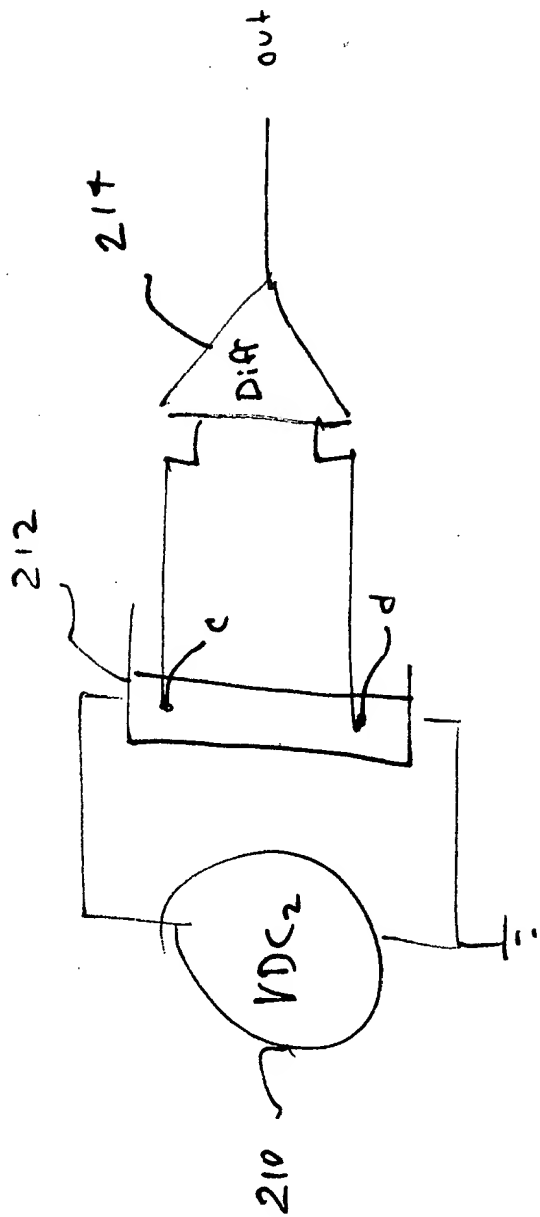


FIG. 5b

108250-05326860

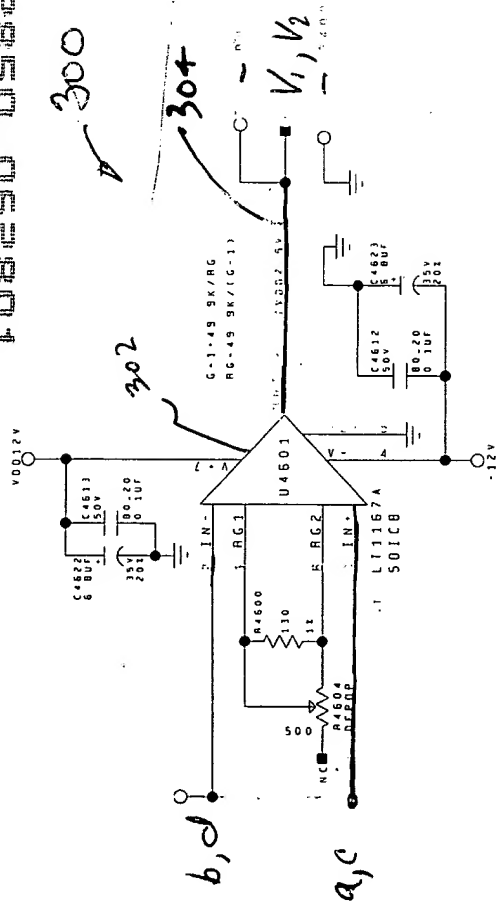
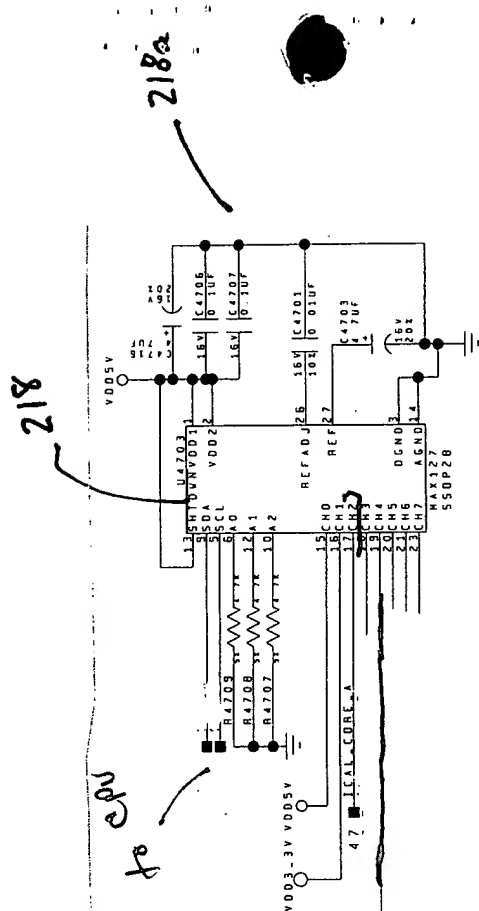
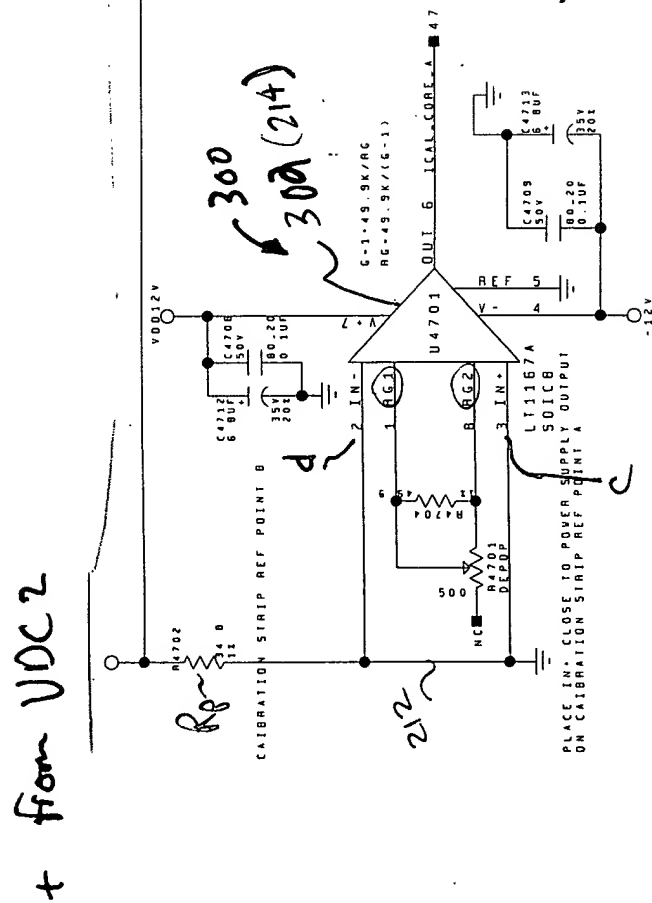


FIG. 6

[illegible]

218

7182

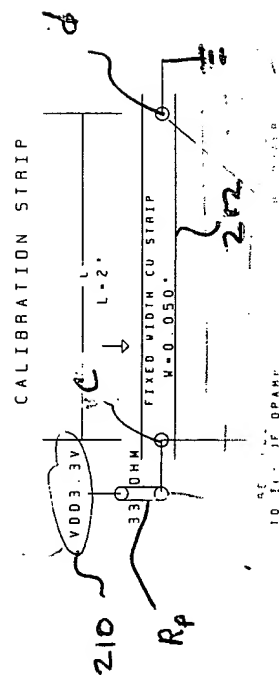


+ from VDC2

300  
214

214

PLACE IN. CLOSE TO POWER SUPPLY OUTPUT  
ON CALIBRATION STRIP REF POINT A



CALIBRATION STRIP

210

210

210

2

2

2

2

2

Fig. 7



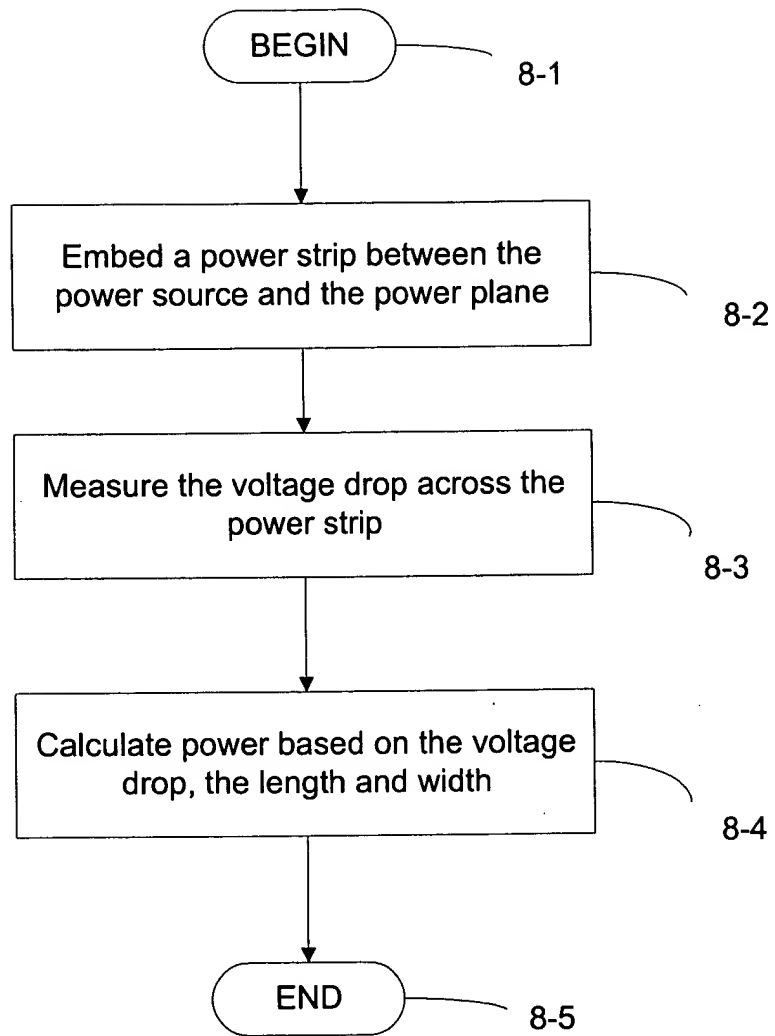


FIG. 8

